mm-Wave Silicon Monolithic Phased Arrays

Bio:

Hossein Hashemi is a Professor of Electrical Engineering, Ming Hsieh Faculty Fellow, and the co-director of the Ming Hsieh Institute and the Ultimate Radio Laboratory (UltRa-Lab) at the University of Southern California. His research interests include analog, mixed-signal, and radio-frequency integrated circuits; electro-optical integrated systems; and implantable integrated solutions. He received the B.S. and M.S. degrees in Electronics Engineering from the Sharif University of Technology, Tehran, Iran, in 1997 and 1999, respectively, and the M.S. and Ph.D. degrees in Electrical Engineering from the California Institute of Technology, Pasadena, in 2001 and 2003, respectively. Dr. Hashemi is an Associate Editor for the IEEE Journal of Solid state Circuits (2013 – present). He was a Distinguished Lecturer for the IEEE Solid-State Circuits Society (2013 – 2014); member of the Technical Program Committee of IEEE International Solid-State Circuits Conference (ISSCC) (2011 – 2015), IEEE Radio Frequency Integrated Circuits (RFIC) Symposium (2011 – present), and the IEEE Compound Semiconductor Integrated Circuits Symposium (CSICS) (2010 – 2014); an Associate Editor for the IEEE Transactions on Circuits and Systems—Part I: Regular Papers (2006–2007) and the IEEE Transactions on Circuits and Systems—Part II: Express Briefs (2004–2005); and Guest Editor for the IEEE Journal of Solid state Circuits (Oct 2013 & Dec 2013). He was the recipient of the 2015 IEEE Microwave Theory and Techniques Society (MTT-S) Outstanding Young Engineer Award, 2008 Defense Advanced Research Projects Agency (DARPA) Young Faculty Award, and a National Science Foundation (NSF) CAREER Award. He received the USC Viterbi School of Engineering Junior Faculty Research Award in 2008, and was recognized as a Distinguished Scholar for the Outstanding Achievement in Advancement of Engineering by the Association of Professors and Scholars of Iranian Heritage in 2011. He was a co-recipient of the 2004 IEEE Journal of Solid-State Circuits Best Paper Award for “A Fully-Integrated 24 GHz 8-Element Phased-Array Receiver in Silicon” and the 2007 IEEE International Solid-State Circuits Conference (ISSCC) Lewis Winner Award for Outstanding Paper for “A Fully Integrated 24 GHz 4-Channel Phased-Array Transceiver in 0.13um CMOS based on a Variable Phase Ring Oscillator and PLL Architecture”. He is the co-editor of the book “Millimeter-Wave Silicon Technology: 60 GHz and Beyond” published by Springer in 2008, and “mm-Wave Silicon Power Amplifiers and Transmitters” published by the Cambridge University Press in 2015.